

WEST Search History

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DATE: Sunday, August 20, 2006

| <u>Hide?</u> | <u>Set Name</u> | <u>Query</u> | <u>Hit Count</u> |
|---------------------------------------|-----------------|--|------------------|
| <i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i> | | | |
| <input type="checkbox"/> | L42 | 5915128.pn. and 144 | 1 |
| <input type="checkbox"/> | L41 | 5915128.pn. and 142 | 1 |
| <input type="checkbox"/> | L40 | 5915128.pn. and phase | 1 |
| <input type="checkbox"/> | L39 | 5915128.pn. and serial | 1 |
| <input type="checkbox"/> | L38 | 5915128.pn. and parallel\$ | 1 |
| <input type="checkbox"/> | L37 | 5915128[uref] | 12 |
| <input type="checkbox"/> | L36 | (speed\$1 match\$3 or speed match\$3) near10 serial\$3 | 19 |
| <input type="checkbox"/> | L35 | flow near3 control\$4 near10 serial\$3 | 1120 |
| <input type="checkbox"/> | L34 | L32 and l1 | 8 |
| <input type="checkbox"/> | L33 | L32 and l12 | 0 |
| <input type="checkbox"/> | L32 | elamin-\$xa. or elamin-\$xp. | 420 |
| <input type="checkbox"/> | L31 | 4920547[uref] | 14 |
| <input type="checkbox"/> | L30 | L26 same ((first or second or domain) near5 (clock or frequency or speed)) | 1 |
| <input type="checkbox"/> | L29 | 5463630[uref] | 12 |
| <input type="checkbox"/> | L28 | L26.ti,ab. | 13 |
| <input type="checkbox"/> | L27 | L26.ti,ab,clm. | 32 |
| <input type="checkbox"/> | L26 | writ\$3 near3 serial\$3 near10 (read\$3 near3 parallel) | 195 |
| <input type="checkbox"/> | L25 | L24 near10 locat\$ | 6 |
| <input type="checkbox"/> | L24 | serial near3 parallel near10 ((first or second or domain) near5 (clock or frequency or speed)) | 675 |
| <input type="checkbox"/> | L23 | L22.ti,ab. | 1 |
| <input type="checkbox"/> | L22 | serial near7 parallel near10 jitter | 43 |
| <input type="checkbox"/> | L21 | serial near7 parallel near10 (bigger or larger) near10 jitter | 2 |
| <input type="checkbox"/> | L20 | L19.ti,ab. | 3 |
| <input type="checkbox"/> | L19 | serial near7 parallel near10 (bigger or larger) | 197 |
| <input type="checkbox"/> | L18 | serial near7 parallel near10 (biger or larger) | 195 |
| <input type="checkbox"/> | L17 | l16 and l1 | 0 |
| <input type="checkbox"/> | L16 | 5619506[uref] | 7 |
| <input type="checkbox"/> | L15 | 5619506.pn. | 1 |
| <input type="checkbox"/> | L14 | 20010021051 and serial\$ | 1 |

| | | | |
|-------------------------------------|-----|---|------|
| <input type="checkbox"/> | L13 | serial near7 parallel near10 (data near3 loss) | 23 |
| <input type="checkbox"/> | L12 | serial near7 parallel near10 (location near3 more) | 3 |
| <input checked="" type="checkbox"/> | L11 | 6829191.pn. and (register) | 1 |
| <input type="checkbox"/> | L10 | 6829191.pn. and (second registers) | 1 |
| <input checked="" type="checkbox"/> | L9 | 6829191.pn. and l1 | 1 |
| <input type="checkbox"/> | L8 | serial near3 parallel near10 (location near3 more) | 3 |
| <input checked="" type="checkbox"/> | L7 | serial near3 parallel near3 (memory or register or buffer) near10 (location near3 more) | 1 |
| <input type="checkbox"/> | L6 | L2 near10 location | 15 |
| <input checked="" type="checkbox"/> | L5 | 5369638[uref] | 0 |
| <input checked="" type="checkbox"/> | L4 | L2 near10 ((first or second or domain) near5 (clock or frequency or speed)) | 24 |
| <input type="checkbox"/> | L3 | L2 same ((first or second or domain) near5 (clock or frequency or speed)) | 64 |
| <input type="checkbox"/> | L2 | L1 near5 (connect\$3 or coupl\$3) | 1316 |
| <input type="checkbox"/> | L1 | serial near3 parallel near3 (memory or register or buffer) | 8743 |

END OF SEARCH HISTORY